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Electronic Version v1.1

Stylesheet Version v1.1.0

Title of Invention	METHOD FOR DESIGNING AN INTEGRATED CIRCUIT HAVING MULTIPLE VOLTAGE DOMAINS	
Application Number :		
Date :		
First Named Applicant:	Joseph A. Iadanza	
Confirmation Number:		
Attorney Docket Number:	BUR920030126US1	
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		Sign. Capacity Attorney

Documents being submitted:	Files
us-assignment	BUR920030126US1-usassn.xml us-assignment.xsl us-assignment.dtd BUR920030126Assign1.tif BUR920030126Assign2.tif
us-request	BUR920030126US1-usrequ.xml us-request.dtd us-request.xsl
us-fee-sheet	BUR920030126US1-usfees.xml us-fee-sheet.xsl us-fee-sheet.dtd
application-body	BUR920030126spec-trans.xml us-application-body.xsl application-body.dtd wipo.ent mathml2.dtd mathml2-qname-1.mod isoamsa.ent isoamsb.ent isoamsc.ent isoamsn.ent isoamso.ent isoamsr.ent isogr3.ent isomfrk.ent isomopf.ent isomscr.ent isotech.ent isobox.ent isocyr1.ent isocyr2.ent isodia.ent isolat1.ent isolat2.ent isonum.ent isopub.ent mmlextra.ent mmlalias.ent soextblx.dtd BUR920030126drw1.tif BUR920030126drw2.tif BUR920030126drw3.tif BUR920030126drw4.tif BUR920030126drw5.tif BUR920030126drw6.tif BUR920030126drw7.tif BUR920030126drw8.tif BUR920030126drw9.tif
us-declaration	BUR920030126Dec1.tif

us-declaration	BUR920030126Dec2.tif
Comments	